

SIOV metal oxide varistors

Housed (ThermoFuse) varistors, AdvanceD series

Series/Type: ETFV20

Date: December 2007



ThermoFuse varistors, ETFV20 series

Construction

- Round varistor element, leaded
- Coating: epoxy resin, flame-retardant to UL 94 V-0
- Terminals: tinned copper wire, metal compound wire
- Housing: thermoplastic, flame-retardant to UL 94 V-0

Features

- Wide operating voltage range 130 ... 420 V_{RMS}
- Self-protected under abnormal overvoltage conditions
- High-energy AdvanceD series E2

Approvals

■ UL

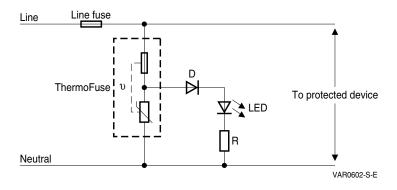
Applications

- Air conditioner, refrigerator, TV, etc.
- Power meter, inverter, telecom equipment, etc.
- Transient voltage surge suppressors (TVSS)

Delivery mode

■ Bulk (standard)

Typical applications



General technical data

Climatic category	to IEC 60068-1	40/85/56	
Operating temperature	to CECC 42 000	-40 + 85	°C
Storage temperature		-40 +125	°C
Electric strength	to CECC 42 000	≥2.5	kV _{RMS}
Response time		<25	ns



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Maximum ratings $(T_A = 85 \, ^{\circ}C)$

Ordering code	Туре	V_{RMS}	V_{DC}	i _{max}	W _{max}	P _{max}
	(untaped)			(8/20 μs)	(2 ms)	
	SIOV-	V	V	Α	J	W
B72220T2131K101	ETFV20K130E2	130	170	10000	100	1.0
B72220T2141K101	ETFV20K140E2	140	180	10000	110	1.0
B72220T2151K101	ETFV20K150E2	150	200	10000	120	1.0
B72220T2171K101	ETFV20K175E2	175	225	10000	135	1.0
B72220T2211K101	ETFV20K210E2	210	270	10000	160	1.0
B72220T2231K101	ETFV20K230E2	230	300	10000	180	1.0
B72220T2251K101	ETFV20K250E2	250	320	10000	195	1.0
B72220T2271K101	ETFV20K275E2	275	350	10000	215	1.0
B72220T2301K101	ETFV20K300E2	300	385	10000	250	1.0
B72220T2321K101	ETFV20K320E2	320	420	10000	273	1.0
B72220T2351K101	ETFV20K350E2	350	460	10000	273	1.0
B72220T2381K101	ETFV20K385E2	385	505	10000	273	1.0
B72220T2421K101	ETFV20K420E2	420	560	10000	273	1.0

Characteristics ($T_A = 25$ °C)

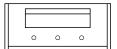
Ordering code	Туре	V_{v}	ΔV_{v}	V _{c, max}	i _c	C _{typ}
	(untaped)	(1 mA)	(1 mA)	(i _c)		(1 kHz)
	SIOV-	V	%	V	Α	pF
B72220T2131K101	ETFV20K130E2	205	±10	340	100	1340
B72220T2141K101	ETFV20K140E2	220	±10	360	100	1240
B72220T2151K101	ETFV20K150E2	240	±10	395	100	1160
B72220T2171K101	ETFV20K175E2	270	±10	455	100	1000
B72220T2211K101	ETFV20K210E2	330	±10	545	100	835
B72220T2231K101	ETFV20K230E2	360	±10	595	100	760
B72220T2251K101	ETFV20K250E2	390	±10	650	100	700
B72220T2271K101	ETFV20K275E2	430	±10	710	100	630
B72220T2301K101	ETFV20K300E2	470	±10	775	100	580
B72220T2321K101	ETFV20K320E2	510	±10	840	100	540
B72220T2351K101	ETFV20K350E2	560	±10	910	100	500
B72220T2381K101	ETFV20K385E2	620	±10	1025	100	450
B72220T2421K101	ETFV20K420E2	680	±10	1120	100	420

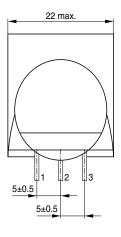


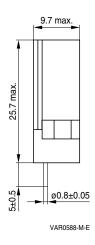


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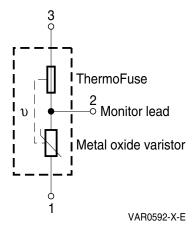
Dimensional drawing







Lead configuration



Weight

Nominal diameter	V_{RMS}	Weight
mm	V	g
20	130 420	6.7 8.3



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Reliability data

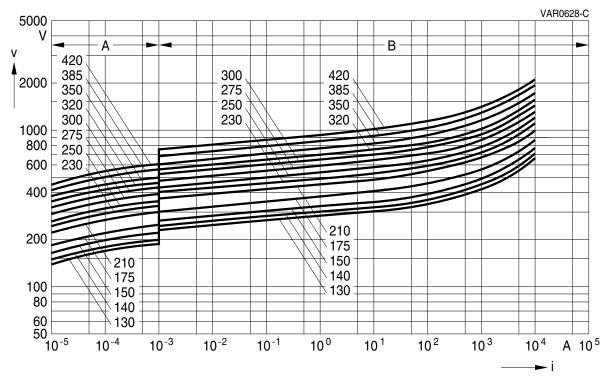
Test	Test methods/conditions	Requirement		
Varistor voltage	The voltage between two terminals with the specified measuring current applied is called V_v (1 mA _{DC} @ 0.2 2 s).	To meet the specified value		
Clamping voltage	The maximum voltage between two terminals with the specified standard impulse current (8/20 µs) applied.	To meet the specified value.		
Surge current derating,	CECC 42 000, test C 2.1	∆V/V (1 mA) ≤10%		
8/20 μs	100 surge currents (8/20 μs), unipolar, interval 30 s, amplitude corresponding to derating curve for 100 impulses	(measured in direction of surge current) No visible damage		
	at 20 μs			
Surge current derating,	CECC 42 000, test C 2.1	∆V/V (1 mA) ≤10%		
2 ms	100 surge currents (2 ms), unipolar, interval 120 s, amplitude corresponding	(measured in direction of surge current)		
	to derating curve for 100 impulses at 2 ms	No visible damage		
Abnormal overvoltage test	UL1449, limited current abnormal overvoltage test. Apply a high AC voltage to ThermoFuse varistor, the amplitude of overvoltage and current limit will be adopted from UL1449 general instruction in section 37. The specimen will be tested on a softwood surface covered with a double layer of white tissue paper. The specimen is to be loosely draped with a double layer of cheese-cloth. The cheesecloth shall cover openings where flame, molten or other particles may be expelled as a result of the test. The test result will be visually inspected.	Any of these phenomena shall not be observed, or this specimen will be judged as failed part. 1. Emission of flame, molten metal, glowing or flaming particles through any openings (pre-existed or created as a result of the test) in the product. 2. Charring, glowing, or flaming of the supporting surface, tissue paper, or cheesecloth. 3. Ignition of the enclosure. 4. Creation of any openings in the enclosure that result in accessibility of live parts, when judge in accordance with accessibility of live parts,		





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v/i characteristics



SIOV-ETFV20 ... E2



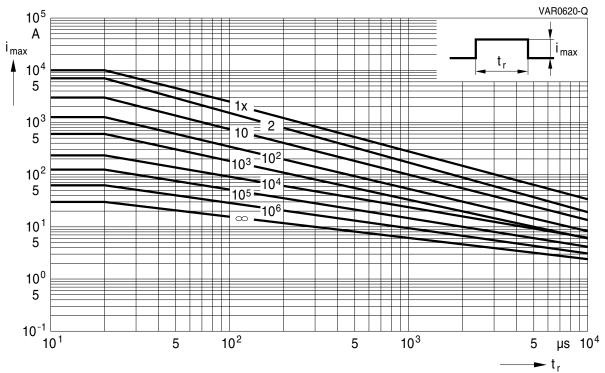
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Derating curves

Maximum surge current $i_{max} = f(t_r, pulse train)$

For explanation of the derating curves refer to "General technical information", section 1.8.1



SIOV-ETFV20 ... E2



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Cautions and warnings

General

- EPCOS metal oxide varistors (SIOVs) are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
- 2. Ensure suitability of SIOVs through reliability testing during the design-in phase. SIOVs should be evaluated taking into consideration worst-case conditions.
- 3. For applications of SIOVs in line-to-ground circuits based on various international and local standards there are restrictions existing or additional safety measures required.

Storage

- 1. Store SIOVs only in original packaging. Do not open the package before storage.
- 2. Storage conditions in original packaging:

Storage temperature: -25 °C ... +45 °C

Relative humidity: <75% annual average,

<95% on maximum 30 days a year.

Dew precipitation: Is to be avoided.

- 3. Avoid contamination of an SIOV's surface during storage, handling and processing.
- 4. Avoid storage of SIOVs in harmful environments that can affect the function during long-term operation (examples given under operation precautions).
- 5. The SIOV type series should be soldered within the time specified:

SIOV-S, -Q, -LS 24 months ETFV and SFS types 12 months.

Handling

- 1. SIOVs must not be dropped.
- 2. Components must not be touched with bare hands. Gloves are recommended.
- 3. Avoid contamination of the surface of SIOV electrodes during handling, be careful of the sharp edge of SIOV electrodes.

Soldering (where applicable)

- 1. Use rosin-type flux or non-activated flux.
- 2. Insufficient preheating may cause ceramic cracks.
- 3. Rapid cooling by dipping in solvent is not recommended.
- 4. Complete removal of flux is recommended.



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Mounting

- 1. Potting, sealing or adhesive compounds can produce chemical reactions in the SIOV ceramic that will degrade the component's electrical characteristics.
- 2. Overloading SIOVs may result in ruptured packages and expulsion of hot materials. For this reason SIOVs should be physically shielded from adjacent components.

Operation

- 1. Use SIOVs only within the specified temperature operating range.
- 2. Use SIOVs only within the specified voltage and current ranges.
- 3. Environmental conditions must not harm SIOVs. Use SIOVs only in normal atmospheric conditions. Avoid use in the presence of deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas, etc), corrosive agents, humid or salty conditions. Avoid contact with any liquids and solvents.



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