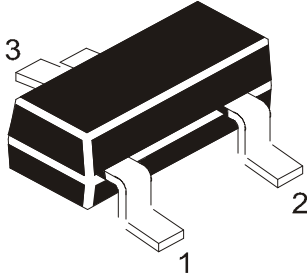


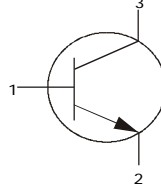
NPN SILICON PLANAR EPITAXIAL TRANSISTORS

BC846, BC847, BC848



PIN CONFIGURATION (NPN)

- 1 = BASE
- 2 = EMITTER
- 3 = COLLECTOR



SOT-23
Formed SMD Package
 For Lead Free Parts, Device Part #
 will be Prefixed with "T"

Marking

- BC846 =1D
- BC846A=1A
- BC846B=1B
- BC847 =1H
- BC847A=1E
- BC847B=1F
- BC847C=1G
- BC848 =1M
- BC848A=1J
- BC848B=1K
- BC848C=1L

For use in Driver Stages of Audio Amplifier in Thick and Thin-film Hybrid Circuits

ABSOLUTE MAXIMUM RATINGS (T_a=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	BC846	BC847	BC848	UNITS
Collector Base Voltage	V _{CBO}	80	50	30	V
Collector Emitter Voltage	V _{CEO}	65	45	30	V
Emitter Base Voltage	V _{EBO}	6	6	5	V
Collector Current (DC)	I _C	100			mA
Collector Current - Peak	I _{CM}	200			mA
Power Dissipation	P _{tot}	250			mW
Storage Temperature	T _{stg}	- 65 to +150			°C
Junction Temperature	T _j	150			°C

ABSOLUTE MAXIMUM RATINGS (T_a=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
DC Current Gain	h _{FE}	I _C =2mA, V _{CE} =5V				
		A	110		220	
		B	200		450	
		C	420		800	
Collector Emitter Saturation Voltage	V _{CE(Sat)}	I _C =10mA, I _B =0.5mA			0.25	V
		I _C =100mA, I _B =5mA			0.60	V
Base Emitter on Voltage	V _{BE(on)}	I _C =2mA, V _{CE} =5V	0.58		0.70	V
		I _C =10mA, V _{CE} =5V			0.72	V
Collector Cut off Current	I _{CBO}	V _{CB} =30V, I _E =0			15	nA
Transition Frequency	f _T	I _C =10mA, V _{CE} =5V, f=100MHz		300		MHz
Output Capacitance	C _{ob}	V _{CB} =10V, f=1MHz			6.0	pF
Input Capacitance	C _{ib}	V _{EB} =0.5V, f=1MHz		9.0		pF
Noise Figure	NF	I _C =0.2mA, V _{CE} =5V R _S =2kΩ, f=1KHz			10	dB

SOT-23
Formed SMD Package
For Lead Free Parts, Device Part #
will be Prefixed with "T"**Component Disposal Instructions**

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.**
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).**

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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