

Vishay Semiconductors

RoHS

HALOGEN

FREE

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°C

## Schottky Rectifier, 2 x 20 A

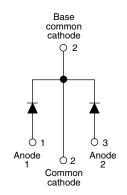


**TO-247AC** 

 $V_{\mathsf{RRM}}$ 

I<sub>FSM</sub>

 $T_{J}$ 



PRODUCT SUMMARY					
Package	TO-247AC				
I <sub>F(AV)</sub>	2 x 20 A				
V <sub>R</sub>	45 V				
V <sub>F</sub> at I <sub>F</sub>	0.56 V				
I <sub>RM</sub> max.	85 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
Diode variation	Common cathode				
E <sub>AS</sub>	20 mJ				

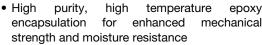
 $t_p = 5 \mu s sine$ 

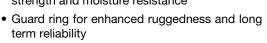
Range

20 Apk, T<sub>J</sub> = 125 °C

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- Very low forward voltage drop
- High frequency operation





- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)



The VS-MBR4045WT... center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

45 1020

0.56

- 55 to 150

Diode variation  E <sub>AS</sub>	Common cathode 20 mJ	<i>3</i> 1	pplications are in swit , freewheeling diodes,	0 1 11
MAJOR RATINGS A	ND CHARACTERISTICS			
SYMBOL	CHARACTERISTIC	s	VALUES	UNITS
I <sub>F(AV)</sub>	Rectangular waveform (per devi	ice)	40	A
I <sub>FRM</sub>	T <sub>C</sub> = 125 °C (per leg)		40	A

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-MBR4045WTPbF	VS-MBR4045WT-N3	UNITS
Maximum DC reverse voltage	V <sub>R</sub>	4E	45	V
Maximum working peak reverse voltage	$V_{RWM}$	45	45	V

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average per	er leg	T <sub>C</sub> = 125 °C, 50 % duty cycle, rectangular waveform		T 105 % 50 % ditti avala mastanavilari vasta		20	
forward current per dev	ice I <sub>F(AV)</sub>			40			
Peak repetitive forward current per leg	I <sub>FRM</sub>	Rated V <sub>R</sub> , square wave, 20 kHz, T <sub>C</sub> = 125 °C		40	Α		
Maximum peak one cycle non-repetitive surge current per leg	leo, ,	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1020			
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	265			
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 3 A, L = 4.40 mH		20	mJ		
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		3	Α		

Revision: 30-Aug-11 Document Number: 94295



# VS-MBR4045WTPbF, VS-MBR4045WT-N3

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
		20 A	T <sub>.1</sub> = 25 °C	0.59	V	
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	40 A	1j=25 C	0.78		
Maximum forward voltage drop	VFM (1)	20 A	T _ 105 °C	0.56		
		40 A	T <sub>J</sub> = 125 °C	0.72		
	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C		1.75	mA	
Maximum instantaneous reverse current		T <sub>J</sub> = 100 °C	Rated DC voltage	50		
		T <sub>J</sub> = 125 °C		85		
Threshold voltage	V <sub>F(TO)</sub>	T. – T. movimum		0.29	V	
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum		10.3	mΩ	
Maximum junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		900	pF	
Typical series inductance	L <sub>S</sub>	Measured from top of term	7.5	nΗ		
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs	

### Note

 $^{(1)}\,$  Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction temperature range	$T_J$		- 55 to 150	°C		
Maximum storage temperature range	T <sub>Stg</sub>		- 55 to 175	10		
Maximum thermal resistance, junction to case per package	R <sub>thJC</sub>	DC operation	1.4	°C/W		
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.7	C/VV		
Approximate weight			6	g		
Approximate weight			0.21	OZ.		
Mounting torque minimum			6 (5)	kgf · cm		
maximum			12 (10)	(lbf $\cdot$ in)		
Device marking		Case style TO-247AC (JEDEC)	MBR4	045WT		



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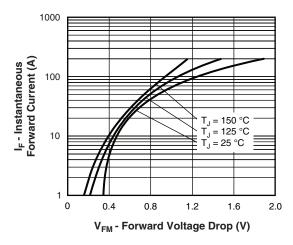


Fig. 1 - Maximum Forward Voltage Drop Characteristics

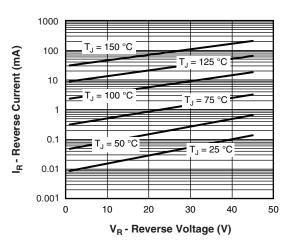


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

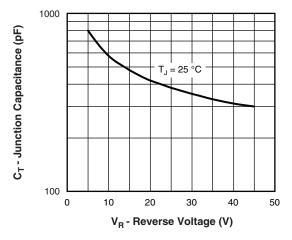


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

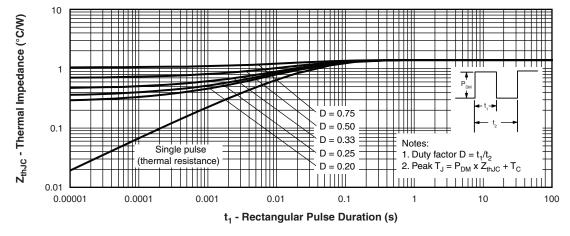


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics



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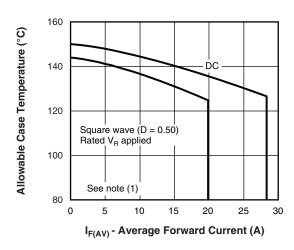


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

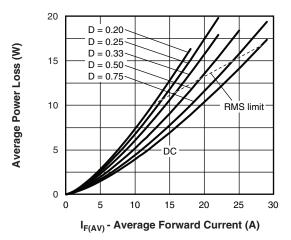


Fig. 6 - Forward Power Loss Characteristics

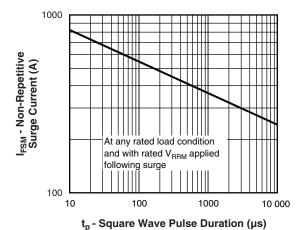


Fig. 7 - Maximum Non-Repetitive Surge Current

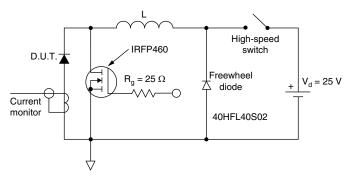


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

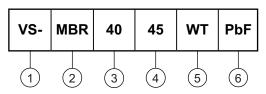
 $\begin{array}{l} \text{(1)} \ \ \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \ at \ (I_{F(AV)}/D) \ (\text{see fig. 6}); \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \ (1 - D); \ I_R \ at \ V_{R1} = Rated \ V_R \ ) \\ \end{array}$ 

## VS-MBR4045WTPbF, VS-MBR4045WT-N3

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### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Schottky MBR series

3 - Current rating (40 = 40 A)

4 - Voltage rating (45 = 45 V)

- Circuit configuration:

Center tap (dual) TO-247

6 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

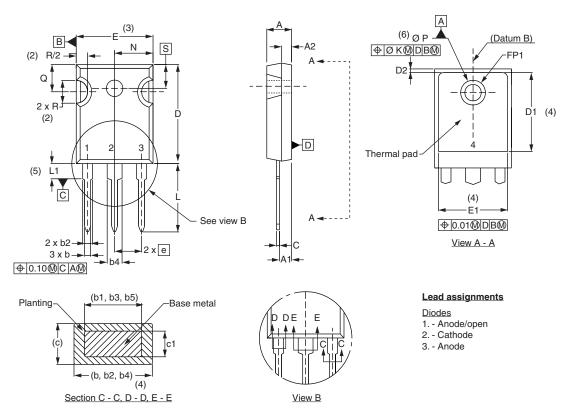
ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-MBR4045WTPbF	25	500	Antistatic plastic tube			
VS-MBR4045WT-N3	25	500	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95223</u>					
Dout moulding information	TO-247AC PbF	www.vishay.com/doc?95226			
Part marking information	TO-247AC -N3	www.vishay.com/doc?95007			
SPICE model		www.vishay.com/doc?95297			



## Vishay Semiconductors

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	IETERS	INCHES		NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.37	0.065	0.094	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.86	0.015	0.034	
c1	0.38	0.76	0.015	0.030	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	BOI MILLIMETERS INCHES		NOTES		
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.72	-	0.540	-	
е	5.46	BSC	0.215	BSC	
FK	2.	54	0.0	010	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
N	7.62	BSC	0	.3	
ΦР	3.56	3.66	0.14	0.144	
ФР1	1	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	1.78	0.216	
S	5.51	BSC	0.217	'BSC	

#### **Notes**

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c



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