# DISCRETE SEMICONDUCTORS

# DATA SHEET

# **BFG541**NPN 9 GHz wideband transistor

**Product specification** 

September 1995



# **BFG541**

#### **FEATURES**

- High power gain
- Low noise figure
- High transition frequency
- Gold metallization ensures excellent reliability.

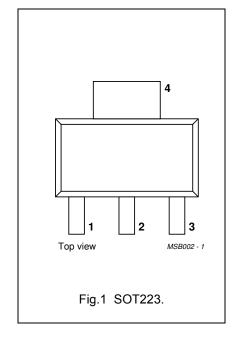
# **DESCRIPTION**

NPN silicon planar epitaxial transistor, intended for wideband applications in the GHz range, such as analog and digital cellular telephones, cordless telephones (CT1, CT2, DECT, etc.), radar detectors, satellite TV tuners (SATV), MATV/CATV amplifiers and repeater amplifiers in fibre-optic systems.

The transistors are mounted in a plastic SOT223 envelope.

#### **PINNING**

| PIN | DESCRIPTION |  |
|-----|-------------|--|
| 1   | emitter     |  |
| 2   | base        |  |
| 3   | emitter     |  |
| 4   | collector   |  |



# NPN 9 GHz wideband transistor

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# **QUICK REFERENCE DATA**

| SYMBOL                         | PARAMETER                             | CONDITIONS   | MIN. | TYP. | MAX. | UNIT |
|--------------------------------|---------------------------------------|--|------|------|------|------|
| V <sub>CBO</sub>               | collector-base voltage                | open emitter   | -    | -    | 20   | ٧    |
| V <sub>CES</sub>               | collector-emitter voltage             | R <sub>BE</sub> = 0  | _    | _    | 15   | V    |
| Ic                             | DC collector current                  |  | _    | _    | 120  | mA   |
| P <sub>tot</sub>               | total power dissipation               | up to T <sub>s</sub> = 140 °C; note 1  | -    | -    | 650  | mW   |
| h <sub>FE</sub>                | DC current gain                       | $I_C = 40 \text{ mA}; V_{CE} = 8 \text{ V}; T_j = 25 ^{\circ}\text{C}$   | 60   | 120  | 250  |      |
| C <sub>re</sub>                | feedback capacitance                  | I <sub>C</sub> = 0; V <sub>CB</sub> = 8 V; f = 1 MHz   | _    | 0.7  | _    | pF   |
| f <sub>T</sub>                 | transition frequency                  | I <sub>C</sub> = 40 mA; V <sub>CE</sub> = 8 V; f = 1 GHz;<br>T <sub>amb</sub> = 25 °C                                      | _    | 9    | _    | GHz  |
| G <sub>UM</sub>                | maximum unilateral power gain         | $I_C$ = 40 mA; $V_{CE}$ = 8 V; f = 900 MHz; $T_{amb}$ = 25 °C  | _    | 15   | _    | dB   |
|                                |                                       | I <sub>C</sub> = 40 mA; V <sub>CE</sub> = 8 V; f = 2 GHz;<br>T <sub>amb</sub> = 25 °C                                      | _    | 9    | _    | dB   |
| S <sub>21</sub>   <sup>2</sup> | insertion power gain                  | $I_C$ = 40 mA; $V_{CE}$ = 8 V; f = 900 MHz; $T_{amb}$ = 25 °C  | 13   | 14   | _    | dB   |
| F                              | noise figure                          | $\Gamma_{\rm s} = \Gamma_{\rm opt}$ ; I <sub>C</sub> = 10 mA; V <sub>CE</sub> = 8 V; f = 900 MHz; T <sub>amb</sub> = 25 °C | _    | 1.3  | 1.8  | dB   |
| P <sub>L1</sub>                | output power at 1 dB gain compression | $I_C$ = 40 mA; $V_{CE}$ = 8 V; $R_L$ = 50 Ω; $f$ = 900 MHz; $T_{amb}$ = 25 °C  | _    | 21   | _    | dBm  |
| ITO                            | third order intercept point           | $I_C$ = 40 mA; $V_{CE}$ = 8 V; $R_L$ = 50 Ω; $f$ = 900 MHz; $T_{amb}$ = 25 °C  | _    | 34   | _    | dBm  |

# **LIMITING VALUES**

In accordance with the Absolute Maximum System (IEC 134).

| SYMBOL           | PARAMETER                 | CONDITIONS                            | MIN. | MAX. | UNIT |
|------------------|---------------------------|---------------------------------------|------|------|------|
| V <sub>CBO</sub> | collector-base voltage    | open emitter                          | _    | 20   | V    |
| V <sub>CES</sub> | collector-emitter voltage | R <sub>BE</sub> = 0                   | _    | 15   | V    |
| $V_{EBO}$        | emitter-base voltage      | open collector                        | _    | 2.5  | V    |
| I <sub>C</sub>   | DC collector current      |                                       | _    | 120  | mA   |
| P <sub>tot</sub> | total power dissipation   | up to T <sub>s</sub> = 140 °C; note 1 | _    | 650  | mW   |
| T <sub>stg</sub> | storage temperature       |                                       | -65  | 150  | °C   |
| Tj               | junction temperature      |                                       | _    | 175  | °C   |

# THERMAL RESISTANCE

| SYMBOL              | PARAMETER   | CONDITIONS                            | THERMAL RESISTANCE |
|---------------------|---|---------------------------------------|--------------------|
| R <sub>th j-s</sub> | thermal resistance from junction to soldering point | up to T <sub>s</sub> = 140 °C; note 1 | 55 K/W             |

# Note

1.  $T_s$  is the temperature at the soldering point of the collector tab.

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#### **CHARACTERISTICS**

T<sub>i</sub> = 25 °C unless otherwise specified.

| SYMBOL                         | PARAMETER                               | PARAMETER CONDITIONS  |    |     | MAX. | UNIT |
|--------------------------------|---|---|----|-----|------|------|
| I <sub>CBO</sub>               | collector cut-off current               | I <sub>E</sub> = 0; V <sub>CB</sub> = 8 V   | _  | _   | 50   | nA   |
| h <sub>FE</sub>                | DC current gain                         | I <sub>C</sub> = 40 mA; V <sub>CE</sub> = 8 V   | 60 | 120 | 250  |      |
| C <sub>e</sub>                 | emitter capacitance                     | I <sub>C</sub> = i <sub>c</sub> = 0; V <sub>EB</sub> = 0.5 V; f = 1 MHz   | _  | 2   | _    | pF   |
| C <sub>c</sub>                 | collector capacitance                   | I <sub>E</sub> = i <sub>e</sub> = 0; V <sub>CB</sub> = 8 V; f = 1 MHz   | _  | 1   | _    | pF   |
| C <sub>re</sub>                | feedback capacitance                    | I <sub>C</sub> = 0; V <sub>CB</sub> = 8 V; f = 1 MHz  | _  | 0.7 | _    | pF   |
| f <sub>T</sub>                 | transition frequency                    | $I_C$ = 40 mA; $V_{CE}$ = 8 V; f = 1 GHz;<br>$T_{amb}$ = 25 °C  | _  | 9   | _    | GHz  |
| G <sub>UM</sub>                | maximum unilateral power gain (note 1)  | $I_C$ = 40 mA; $V_{CE}$ = 8 V; f = 900 MHz; $T_{amb}$ = 25 °C   | _  | 15  | _    | dB   |
|                                |   | $I_C$ = 40 mA; $V_{CE}$ = 8 V; f = 2 GHz; $T_{amb}$ = 25 °C   | _  | 9   | _    | dB   |
| S <sub>21</sub>   <sup>2</sup> | insertion power gain                    | $I_c$ = 40 mA; $V_{CE}$ = 8 V; f = 900 MHz; $T_{amb}$ = 25 °C   |    | 14  | _    | dB   |
| F                              | noise figure                            | $\Gamma_{\rm s}$ = $\Gamma_{\rm opt}$ ; $I_{\rm C}$ = 10 mA; $V_{\rm CE}$ = 8 V; f = 900 MHz; $T_{\rm amb}$ = 25 °C             | _  | 1.3 | 1.8  | dB   |
|                                |   | $\Gamma_{\rm s}$ = $\Gamma_{\rm opt}$ ; I <sub>C</sub> = 40 mA; V <sub>CE</sub> = 8 V;<br>f = 900 MHz; T <sub>amb</sub> = 25 °C | _  | 1.9 | 2.4  | dB   |
|                                |   | $\Gamma_{\rm s}$ = $\Gamma_{\rm opt}$ ; $I_{\rm C}$ = 10 mA; $V_{\rm CE}$ = 8 V; f = 2 GHz; $T_{\rm amb}$ = 25 °C               | _  | 2.1 | _    | dB   |
| P <sub>L1</sub>                | output power at 1 dB gain compression   | $I_c$ = 40 mA; $V_{CE}$ = 8 V; $R_L$ = 50 Ω; $f$ = 900 MHz; $T_{amb}$ = 25 °C   | _  | 21  | _    | dBm  |
| ITO                            | third order intercept point             | note 2  | _  | 34  | -    | dBm  |
| Vo                             | output voltage                          | note 3  | _  | 500 | _    | mV   |
| d <sub>2</sub>                 | second order intermodulation distortion | note 4  | _  | -50 | _    | dB   |

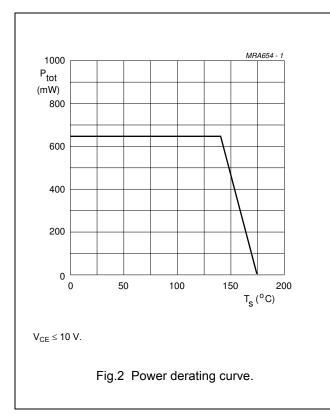
#### **Notes**

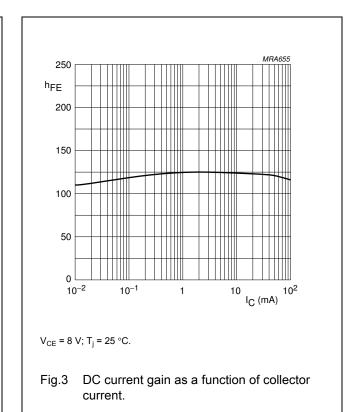
1.  $\,\,G_{UM}$  is the maximum unilateral power gain, assuming  $S_{12}$  is zero and

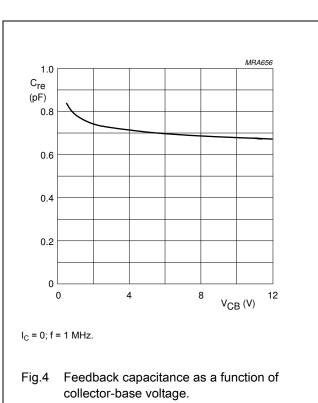
$$G_{UM} = 10 \log \frac{\left|S_{21}\right|^{2}}{(1 - \left|S_{11}\right|^{2})(1 - \left|S_{22}\right|^{2})} dB.$$

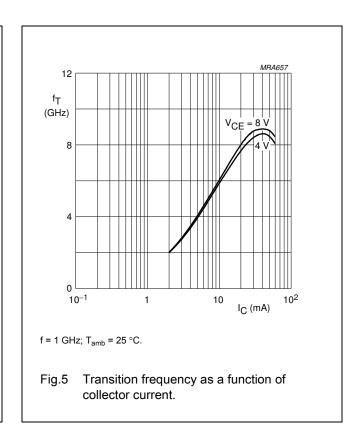
- 2.  $I_{C}$  = 40 mA;  $V_{CE}$  = 8 V;  $R_{L}$  = 50  $\Omega$ ; f = 900 MHz;  $T_{amb}$  = 25 °C;  $f_{p}$  = 900 MHz;  $f_{q}$  = 902 MHz; measured at  $f_{(2p-q)}$  = 898 MHz and at  $f_{(2p-q)}$  = 904 MHz.
- 3.  $d_{im}$  = -60 dB (DIN 45004B);  $I_C$  = 40 mA;  $V_{CE}$  = 8 V;  $Z_L$  =  $Z_s$  = 75  $\Omega$ ;  $T_{amb}$  = 25 °C;  $V_p$  =  $V_o$ ;  $V_q$  =  $V_o$  -6 dB;  $V_r$  =  $V_o$  -6 dB;  $f_p$  = 795.25 MHz;  $f_q$  = 803.25 MHz;  $f_r$  = 805.25 MHz; measured at  $f_{(p+q-r)}$  = 793.25 MHz
- 4.  $I_C$  = 40 mA;  $V_{CE}$  = 8 V;  $V_o$  = 325 mV;  $T_{amb}$  = 25 °C;  $f_p$  = 250 MHz;  $f_q$  = 560 MHz; measured at  $f_{(p+q)}$  = 810 MHz

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In Figs 6 to 9,  $G_{UM}$  = maximum power gain; MSG = maximum stable gain;  $G_{max}$  = maximum available gain.

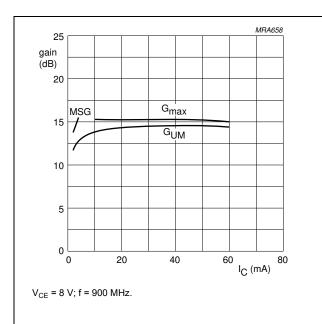
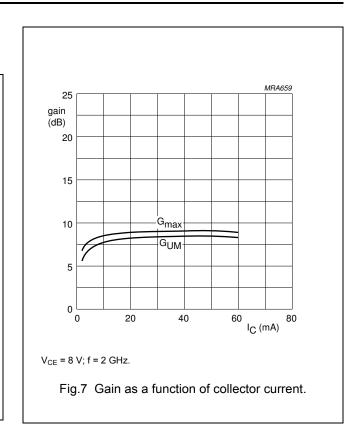
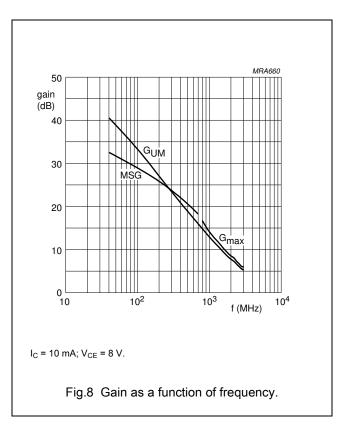
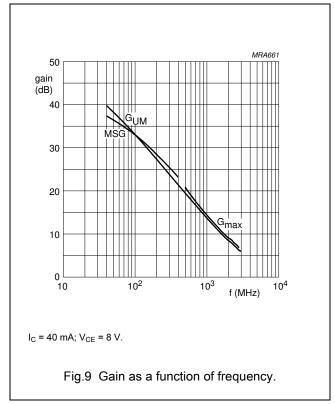


Fig.6 Gain as a function of collector current.







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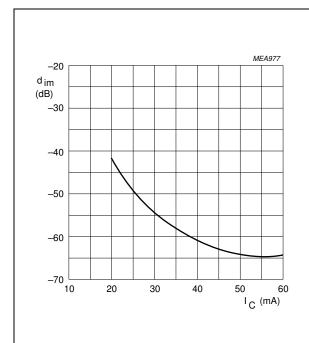


Fig.10 Intermodulation distortion as a function of collector current.

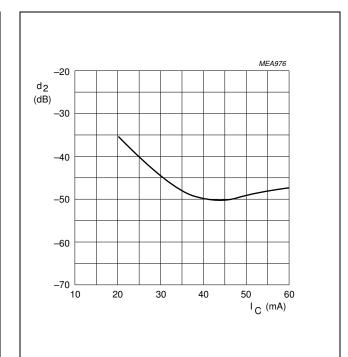


Fig.11 Second order intermodulation distortion as a function of collector current.

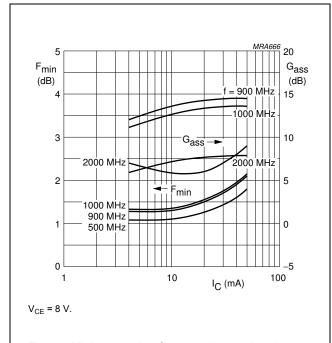


Fig.12 Minimum noise figure and associated available gain as functions of collector current.

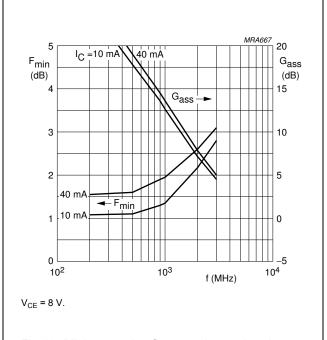
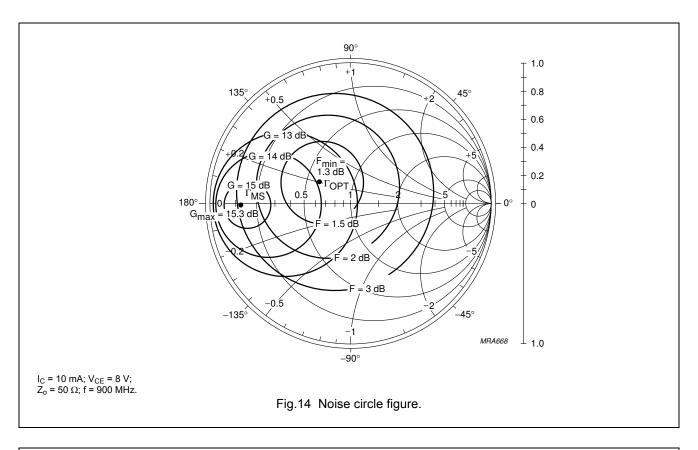
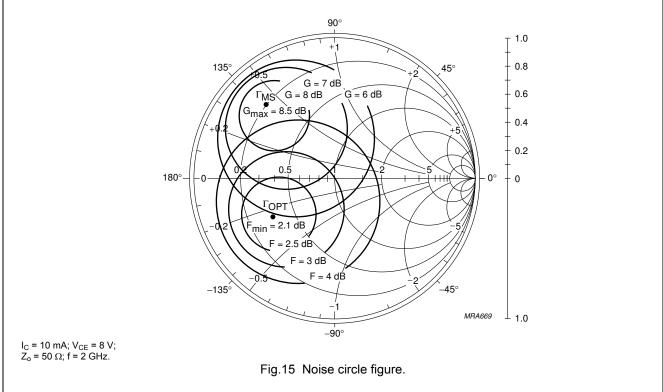
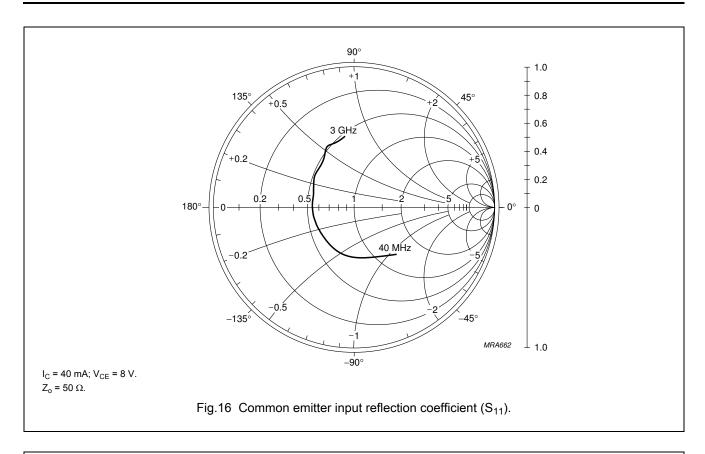


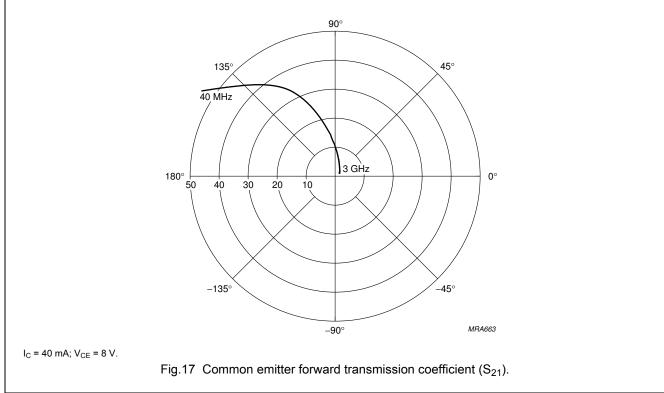
Fig.13 Minimum noise figure and associated available gain as functions of frequency.

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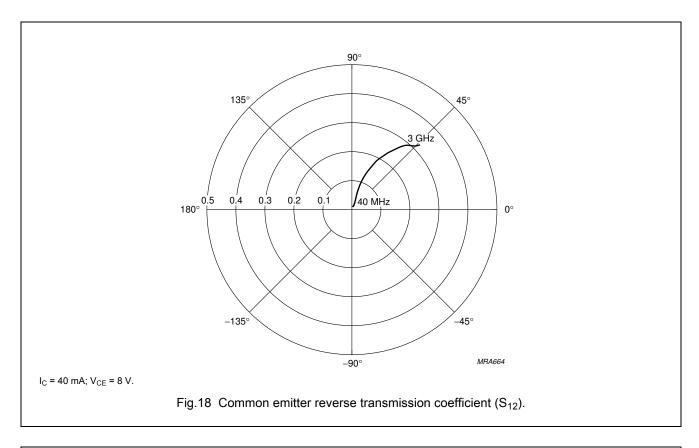


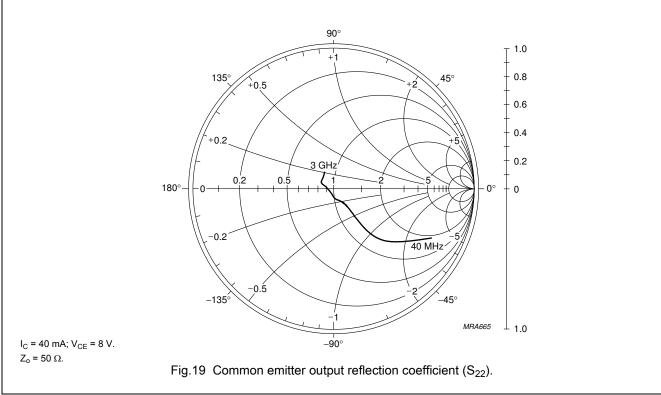






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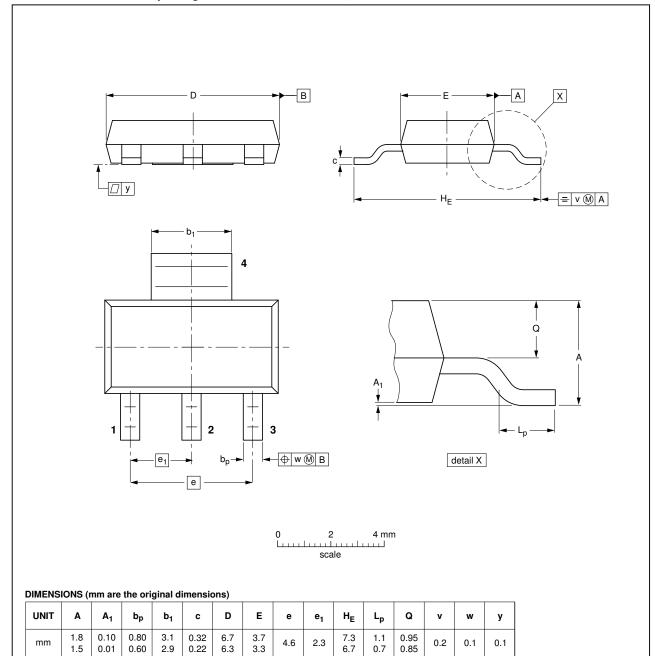


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# **PACKAGE OUTLINE**

# Plastic surface-mounted package with increased heatsink; 4 leads

**SOT223** 



| OUTLINE | OUTLINE |       | EUROPEAN | ISSUE DATE |            |                                  |
|---------|---------|-------|----------|------------|------------|----------------------------------|
| VERSION | IEC     | JEDEC | JEITA    |            | PROJECTION | ISSUE DATE                       |
| SOT223  |         |       | SC-73    |            |            | <del>-04-11-10</del><br>06-03-16 |

2.3

0.2

0.1

4.6

0.01

0.60

2.9

1.5

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| DOCUMENT<br>STATUS <sup>(1)</sup> | PRODUCT<br>STATUS <sup>(2)</sup> | DEFINITION  |
|-----------------------------------|----------------------------------|---|
| Objective data sheet              | Development                      | This document contains data from the objective specification for product development. |
| Preliminary data sheet            | Qualification                    | This document contains data from the preliminary specification.                       |
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